

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
( Not for submission under 37 CFR 1.99)

Application Number	09989777
Filing Date	2001-11-19
First Named Inventor	Craig Nemecek
Art Unit	2123
Examiner Name	Mary C. Jacob
Attorney Docket Number	CD01208M

**U.S.PATENTS**

Examiner Initial*	Cite No	Patent Number	Kind Code <sup>†</sup>	Issue Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	6636096	B1	2003-10-21	Schaffer et al.	Entire Document
	2	6560306	B1	2003-05-06	Duffy et al.	Entire Document
	3	6680632	B1	2004-01-20	Meyers et al.	Entire Document
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	5	6690224	B1	2004-02-10	Moore	Entire Document
	6	6782068	B1	2004-08-24	Wilson et al.	Entire Document
	7	7138868	B1	2006-11-21	Sanchez et al.	Entire Document
	8	6686787	B1	2004-02-03	Ling, KuoK	Entire Document

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	9	6617888	B1	2003-09-09	Volk, Andrew M.	Entire Document
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	14	6949984	B1	2005-09-27	Siniscalchi	Entire Document
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	20	6744323	B1	2004-06-01	Moyal et al.	Entire Document
	21	6704381	B1	2004-03-09	Moyal et al.	Entire Document
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	37	5206582	B1	1993-04-27	Ekstedt et al.	Entire Document
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	42	6963233	B1	2005-11-08	Puccio et al.	Entire Document
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#### U.S.PATENT APPLICATION PUBLICATIONS

Examiner Initial*	Cite No	Publication Number	Kind Code <sup>1</sup>	Publication Date	Name of Patentee or Applicant of cited Document	Pages, Columns, Lines where Relevant Passages or Relevant Figures Appear
	1	20050280453	A1	2005-12-22	Hsieh	Entire Document

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FOREIGN PATENT DOCUMENTS								
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Examiner Initials*	Cite No	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc), date, pages(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>5</sup>
	1	Application Number 10/226,911 (CD02112); "Calibration of Integrated Circuit Time Constants," Gehring et al.; 32 pages	<input type="checkbox"/>
	2	USPTO Notice of Allowance for Application Number 09/943,149 (CD01085) dated 01/12/2004; 7 pages	<input type="checkbox"/>
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	5	USPTO Non-Final Rejection for Application Number 09/943,149 (CD01085) dated 11/20/2002; 7 pages	<input type="checkbox"/>
	6	Application Number 09/943,149 (CD01085); "Method for Phase Locking in a Phase Lock Loop," Moyal et al.; 21 pages	<input type="checkbox"/>
	7	Durham et al., "Integrated Continuous-Time Balanced Filters for 16-bit DSP Interfaces," IEEE, 1993; 6 pages	<input type="checkbox"/>

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	8	Durham et al., "Circuit Architectures for High Linearity Monolithic Continuous-Time Filtering," IEEE, 1992; 7 pages	<input type="checkbox"/>
	9	Durham et al., "High-Linearity Continuous-Time Filter in 5-V VLSI CMOS," IEEE, 1992; 8 pages	<input type="checkbox"/>
	10	Application Number 09/047,595 (CD97115): "Roving Range Control to Limit Receive PLL Frequency of Operation," Scott; 35 pages	<input type="checkbox"/>
	11	Application Number 09/216,460 (CD98099): "Circuit and Method for Controlling an Output of a Ring Oscillator," Abugharbieh et al.; 21 pages	<input type="checkbox"/>
	12	Application Number 09/471,914 (CD99064): "Reference-Free Clock Generator and Data Recovery PLL," Dalmia et al.; 32 pages	<input type="checkbox"/>
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	14	Application Number 10/083,442 (CD01264): "Method/Architecture for a Low Gain PLL with Wide Frequency Range," Meyers et al.; 28 pages	<input type="checkbox"/>
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	16	Application Number 09/893,161 (CD00094): "Architecture of a PLL with Dynamic Frequency Control on a PLD," Moore; 32 pages	<input type="checkbox"/>
	17	Application Number 09/608,753 (CD00018): "PLL Lockout Watchdog," Wilson et al.; 24 pages	<input type="checkbox"/>
	18	Application Number 09/398,956 (CD99049): "Frequency Acquisition Rate Control in Phase Lock Loop Circuits," Moyal et al.; 35 pages	<input type="checkbox"/>

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	19	Application Number 09/747,262 (CD00085): "Linearized Digital Phase-Locked Loop," Williams et al.; 9 pages	<input type="checkbox"/>
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	21	Application Number 09/638,989 (CD98104): "Memory Based Phase Locked Loop," Krishnan; 27 pages	<input type="checkbox"/>
	22	Application Number 09/048,905 (CD95045): "Programmable Clock Generator," Mann et al.; 42 pages	<input type="checkbox"/>
	23	Application Number 08/865,342 (CD95045C): "Programmable Clock Generator," Mann et al.; 41 pages	<input type="checkbox"/>
	24	USPTO Notice of Allowance for Application Number 10/293,392 (CD02135) dated 03/10/2004; 6 pages	<input type="checkbox"/>
	25	USPTO Non-Final Rejection for Application Number 10/293,392 (CD02135) dated 10/16/2003; 6 pages	<input type="checkbox"/>
	26	Application Number 10/293,392 (CD02135): "Low Voltage Receiver Circuit and Method for Shifting the Differential Input Signals of the Receiver Depending on a Common Mode Voltage of the Input Signals," Maher et al.; 20 pages	<input type="checkbox"/>
	27	USPTO Notice of Allowance for Application Number 10/288,003 (CD02108) dated 01/14/2005; 6 pages	<input type="checkbox"/>
	28	USPTO Final Rejection for Application Number 10/288,003 (CD02108) dated 10/06/2004; 6 pages	<input type="checkbox"/>
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